

Time Code Reader

The **Time Code Reader** option for the GMR Series automatically detects and locks to incoming time code using an automatic gain control circuit, reading and synchronizing to SMPTE or IRIG-B time code, as well as IRIG-A and IRIG-E waveforms.

	SMPTE 12M	IRIG-B0 (DCLS, PWM)	IRIG-B1 (AM)	IRIG-A (DCLS)	IRIG-A (AM)	IRIG-E (DCLS)	IRIG-E (AM)	
Rate	24, 25, 30 fps NDF and 29.97 DF	1kHz, 1mS	1kHz, 1mS	10kHz, 0.1mS	10kHz, 0.1mS	100Hz, 10mS	100Hz, 10mS	
Input Level Range	0.2-18 Vpp (2 Vpp _{nom})	0.5-12 Vp (5Vpp _{nom})	0.5-8 Vpp (5 Vpp _{nom})	0.5-12 Vp (5 Vp _{nom})	0.5-8 Vpp (5 Vpp _{nom})	0.5-12 Vp (5 Vp _{nom})	0.5-8 Vpp (5 Vpp _{nom})	
Input Impedance kΩ	>10 kΩ			>10 kΩ				
Mark to Space Ratio	N/A	N/A	3.3:1	N/A	3.3:1	N/A	3.3:1	
Automatic Gain Adjust	Over input level range							
Number of Inputs (SE: Single Ended - Unbalanced, Differential - Balanced)	1 SE, 1 DIFF	1 SE, 1 DIFF	1 SE, 1 DIFF	1 SE, 1 DIFF	1 SE, 1 DIFF	1 SE, 1 DIFF	1 SE, 1 DIFF	
Type	NDF	DCLS	AM 1kHz	DCLS	AM 10kHz	DCLS	AM 100Hz	
Encoding	None, Leitch Date, SMPTE 309M	None or IEEE 1344 year or IRIG STD 200-04						
IRIG Coded Expression IRIG STD 200-04	0 BCD _{TOY} , CF, SBS	N/A	B000	B120	A000	A130	E000	E110
	1 BCD _{TOY} , CF	N/A	B001	B121	A001	A131	E001	E111
	2 BCD _{TOY}	N/A	B002	B122	A002	A132	E002	E112
	3 BCD _{TOY} , SBS	N/A	B003	B123	A003	A133	E003	E113
	4 BCD _{TOY} , BCD _{YEAR} , CF, SBS (IEEE-1344 - 1995 Encoding)	N/A	B004	B124	A004	A134	E004	E114
	5 BCD _{TOY} , BCD _{YEAR} , CF	N/A	B005	B125	A005	A135	E005	E115
	6 BCD _{TOY} , BCD _{YEAR}	N/A	B006	B126	A006	A136	E006	E116
	7 BCD _{TOY} , BCD _{YEAR} , SBS	N/A	B007	B127	A007	A137	E007	E117